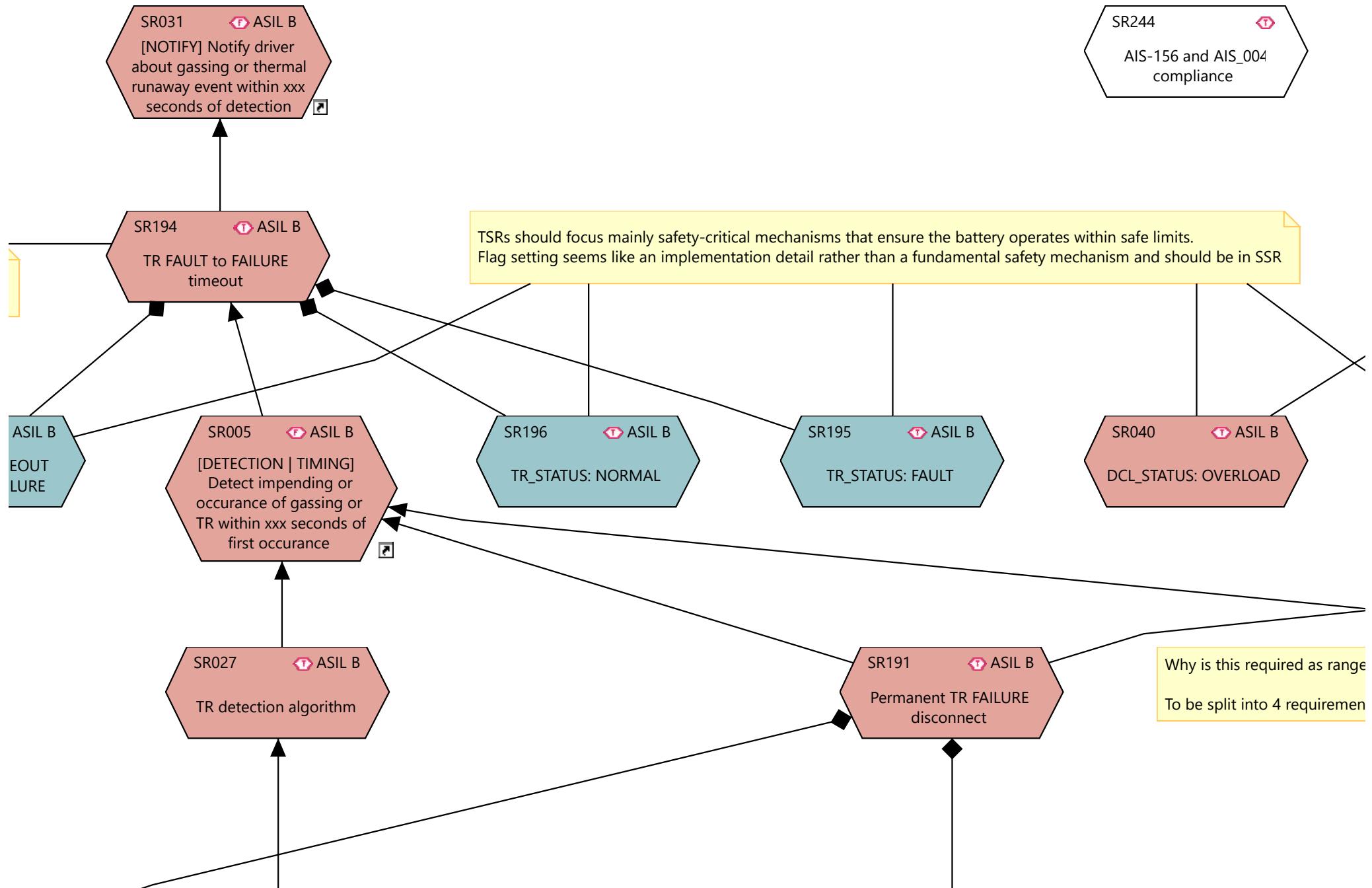
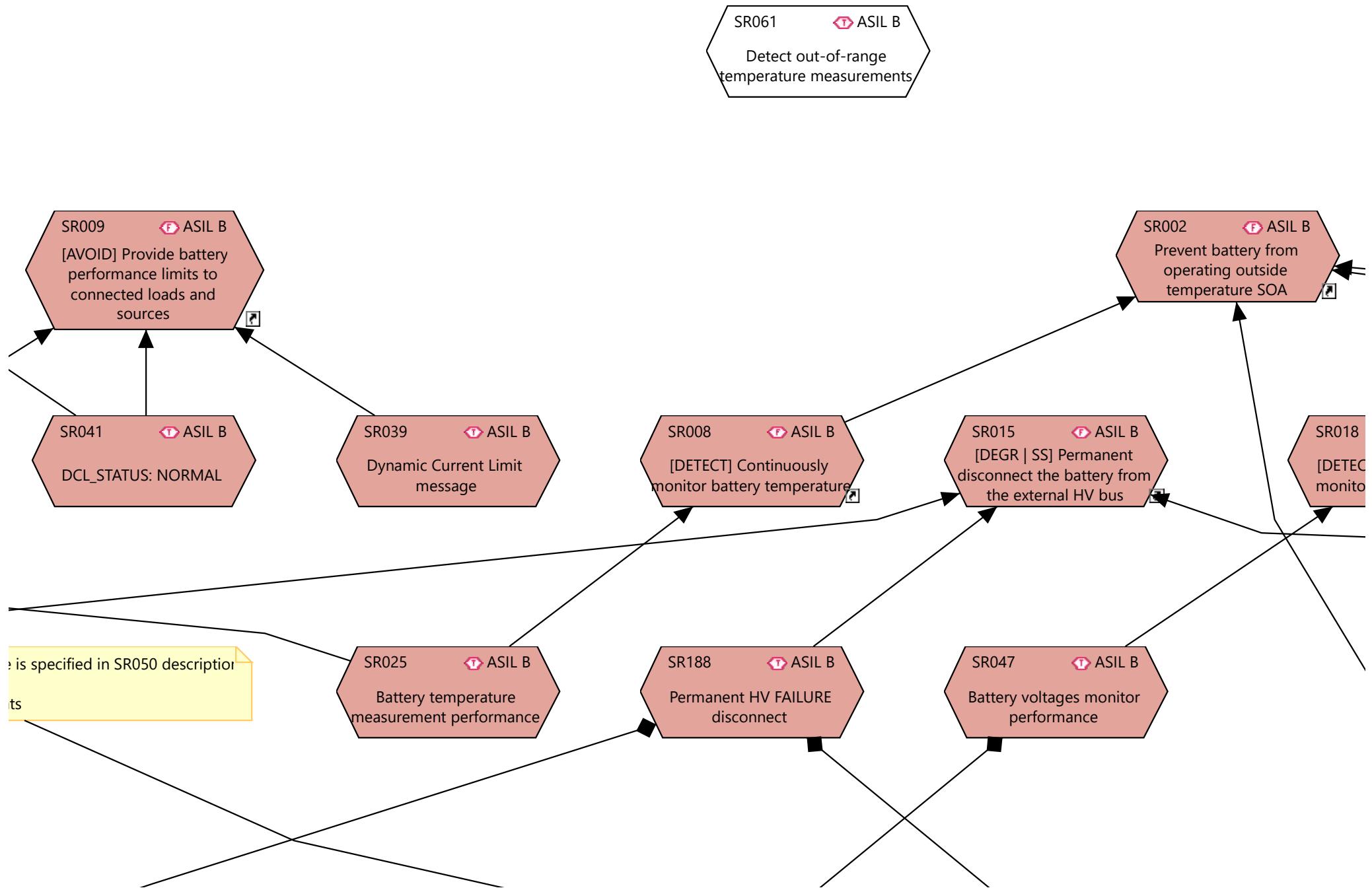
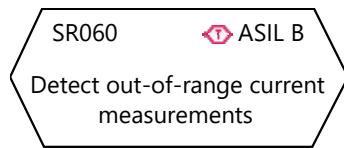


What is the need of both, they seem similar

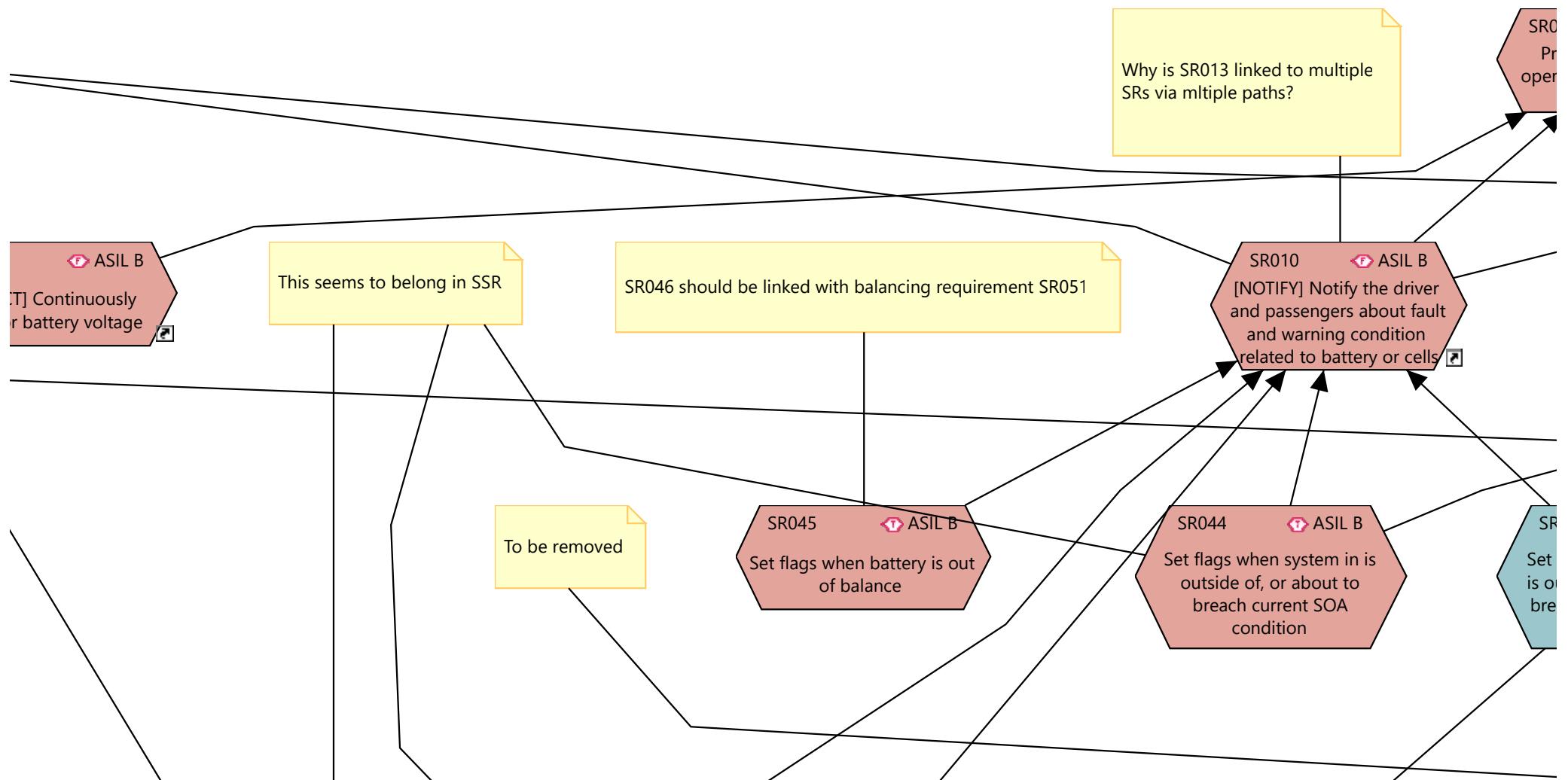
SR197 
TR_FAILURE_TIM
, TR_STATUS: FAI





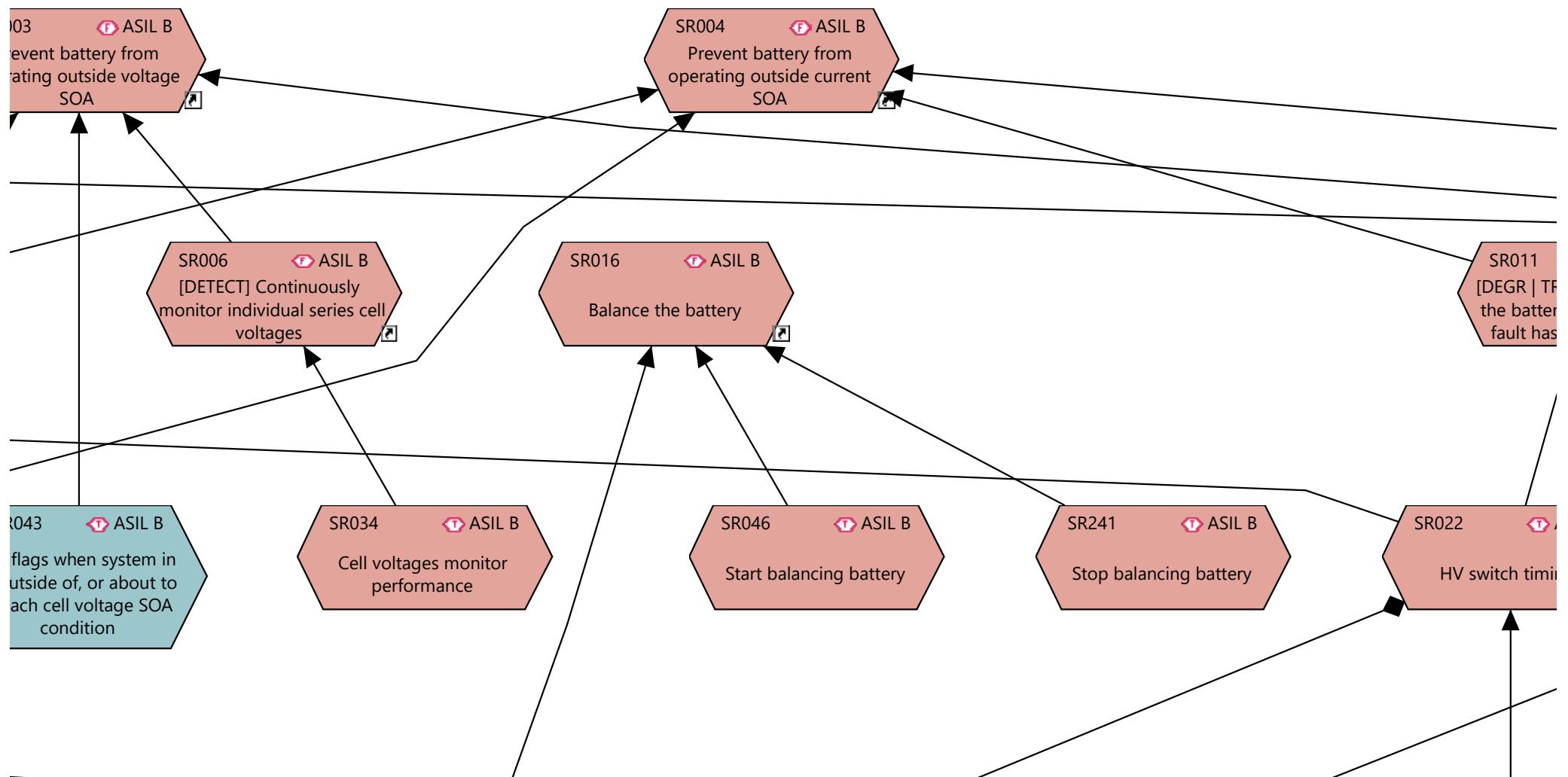


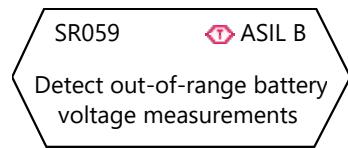
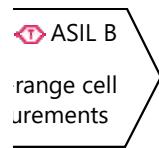
Is this a



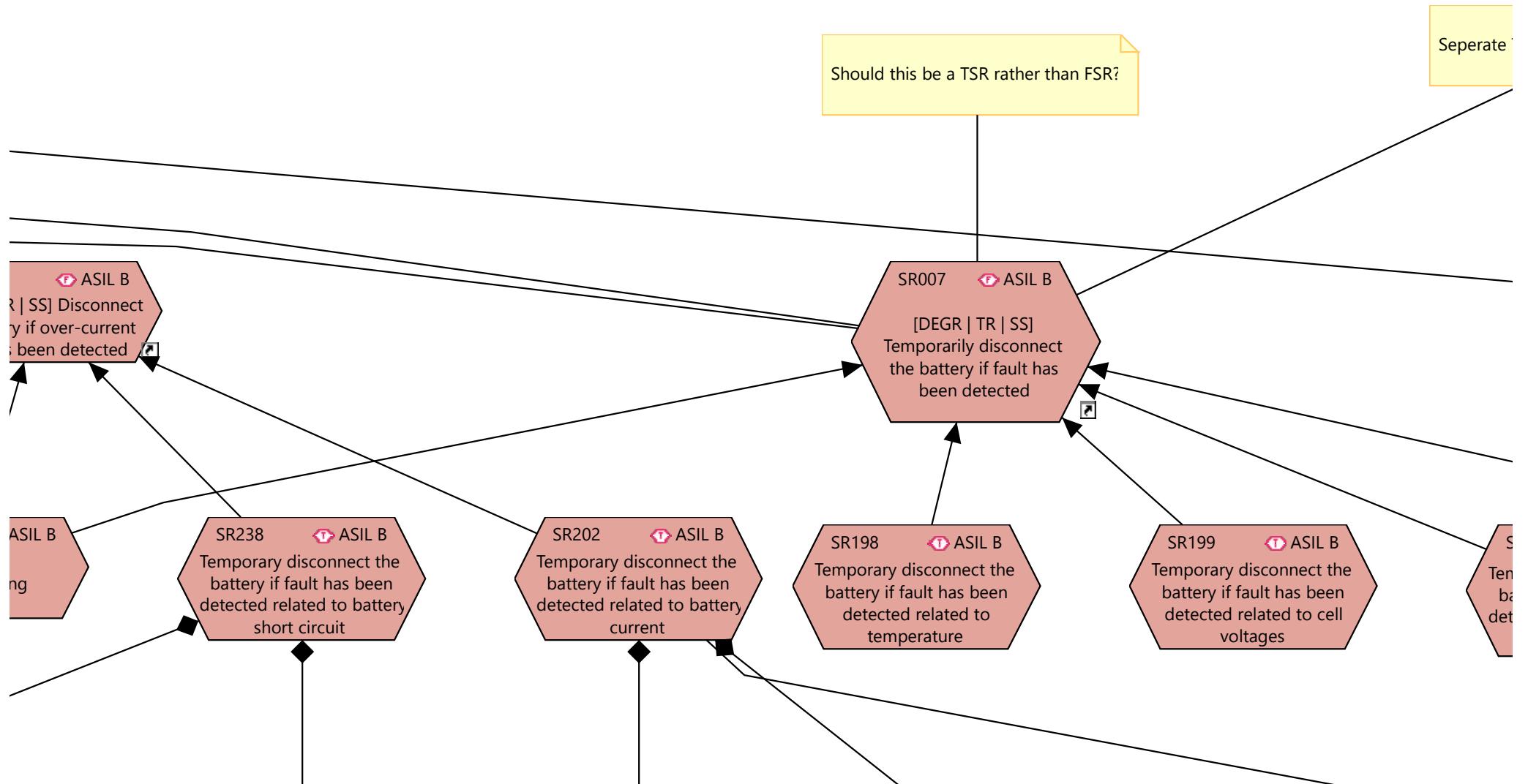
! valid FSR as battery aging and wear affect performance and longevity but do not directly cause safety hazard:

SR063
Detect out-of-
voltage measu





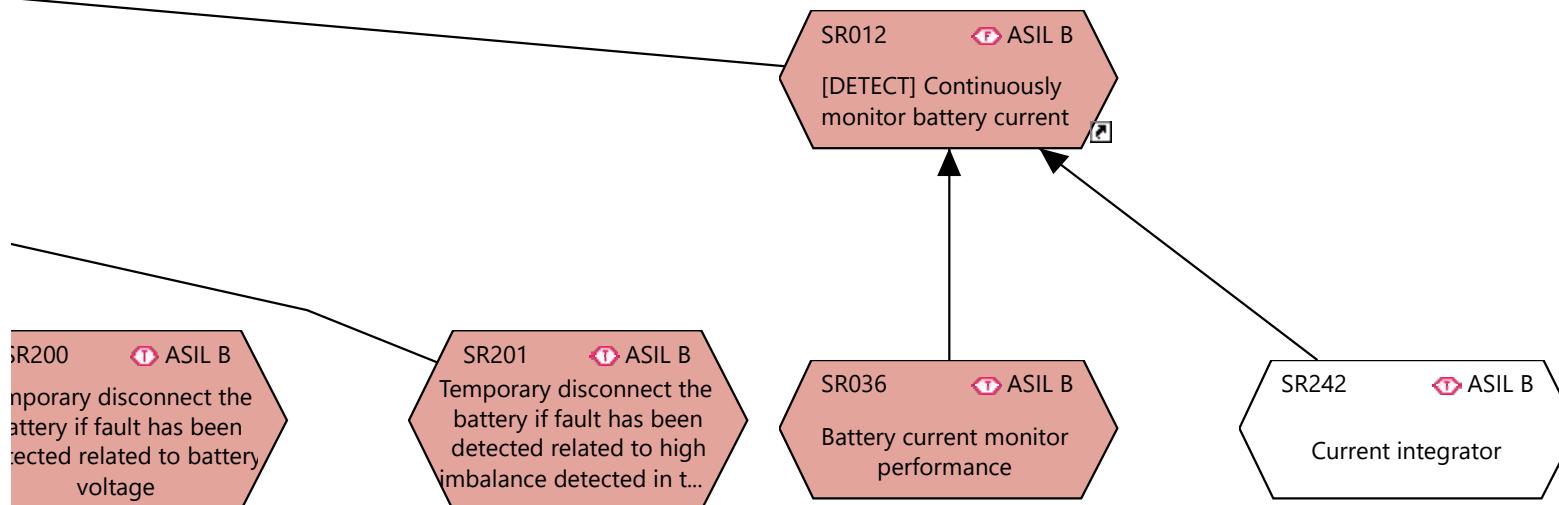
This should be a co



Common requirement for all faults and even other states

TSRs for individual faults of voltages and temperatures

Why are there separate TSR for each current fault ?
Why not compile those with voltage/temp related disconnects or
why not have separate TSR for each voltage/temp fault

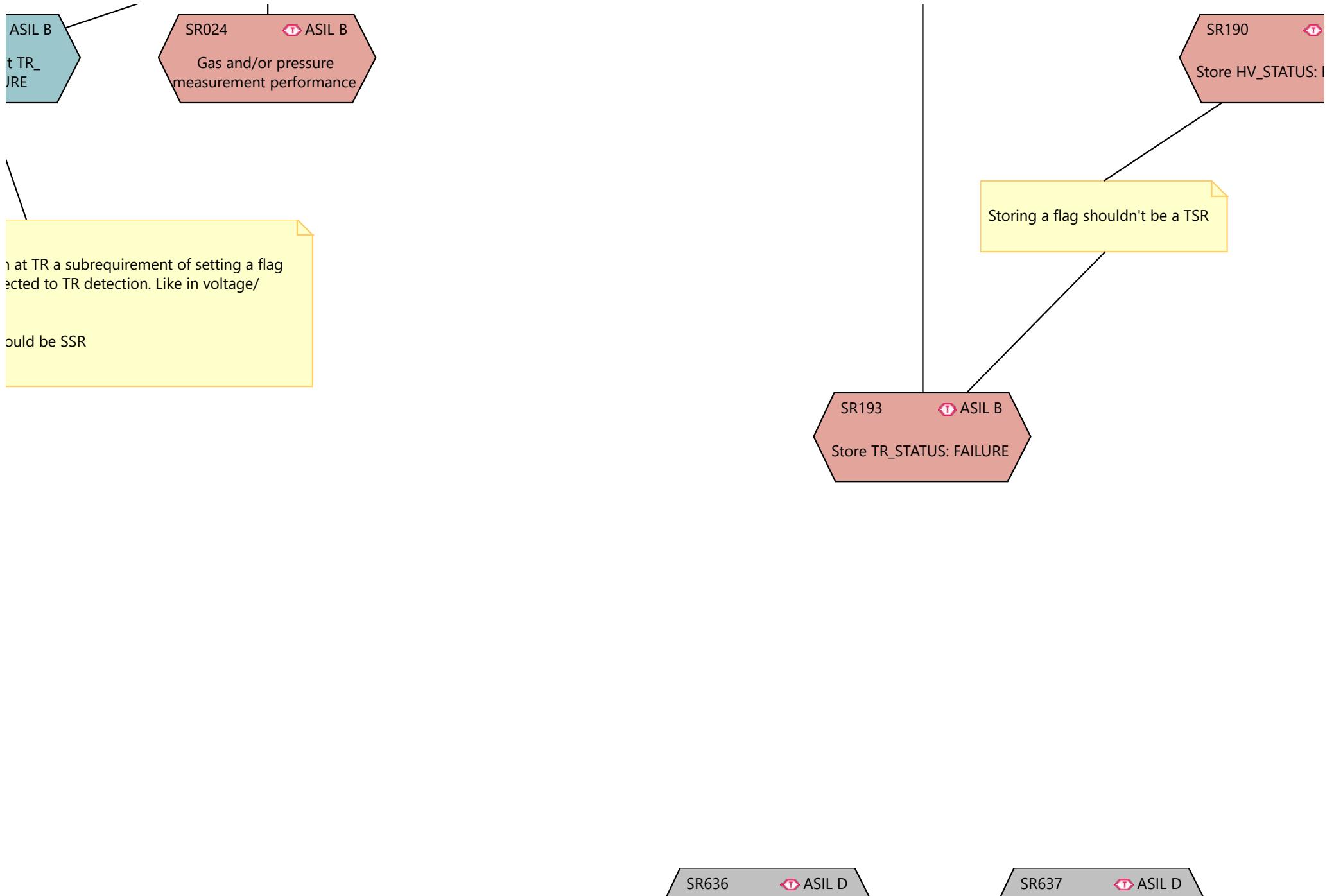


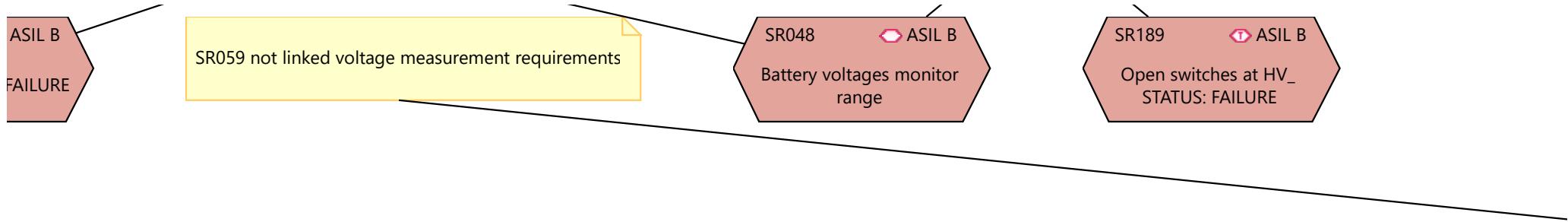
SR192 

Open switches a
STATUS: FAILU

Why is opening switch
and not directly connect
temp cases

REMOVE THIS, this sh



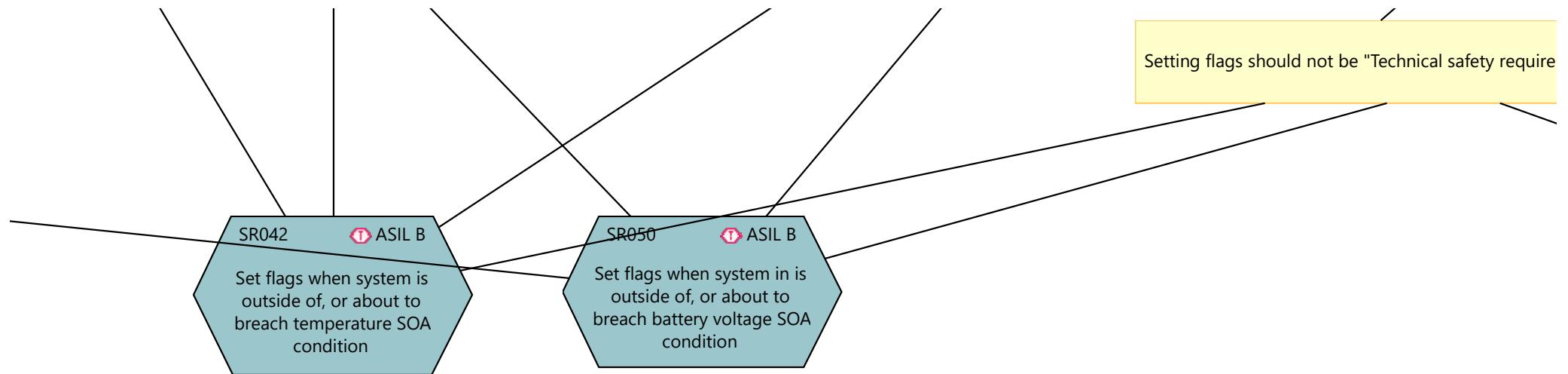


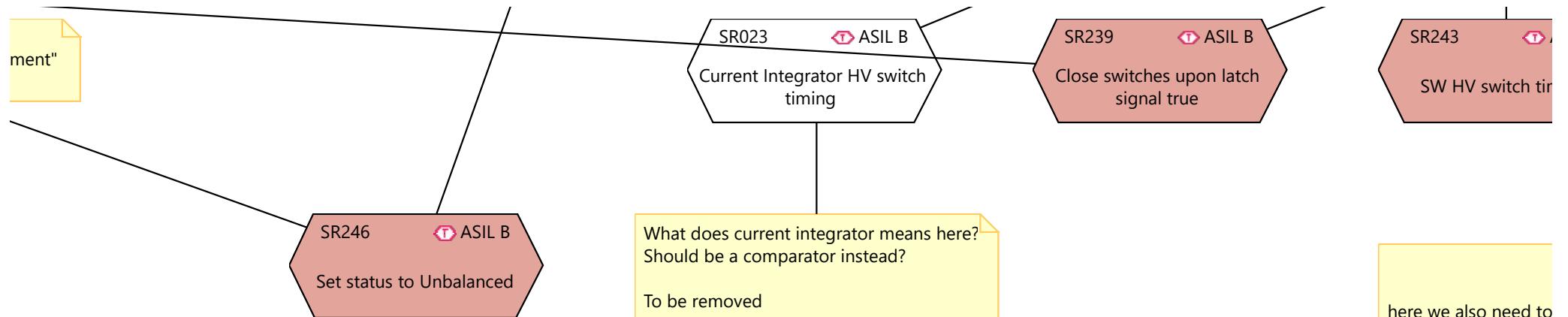
I think they should be Functional Requirements instead, like we have added thermal runaway detection.
Then 30 days logging will be QM or ASIL rated

TSR will also contain high-level
safety regulatory requirements
such as AIS156

I think they should be Functional Requirements instead, like we have added thermal runaway detection.
Then 30 days logging will be QM or ASIL rated

TSR will also contain high-level
safety regulatory requirements
such as AIS156





here we also need to
each fault.

Since this is TSR, we :
of exit from safe state

And also define what
of those safe states

ASIL B
ning

SR240 ASIL B
Short-circuit definition

SR203 ASIL B
Open switches at BAT_
CURR_STATUS: FAILURE

SR204 ASIL B
Close switches at BAT_
CURR_STATUS: NORMAL

Break down into OCC and OCD rather tha

define the "safe state" for
should also define methods
e
activities take place in each

Recovery TSRs added

Temporary disconnect added in FSRs

an flags

Reliability requirement:
signal connectors

Reliability requirement
shunt

SR635  ASIL D
Reliability requirement: main
mosfets

SR638  ASIL D
Reliability requirement
power connector

SR634  ASIL D
Reliability requirement: fuse

SR632  ASIL D
Reliability requirement:
precharge resistor

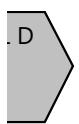
SR633  ASIL D
Reliability requirement:
precharge mosfet

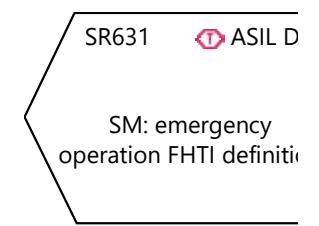
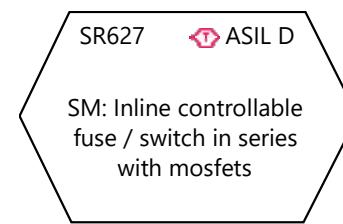
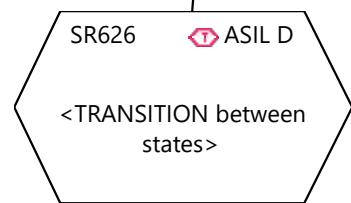
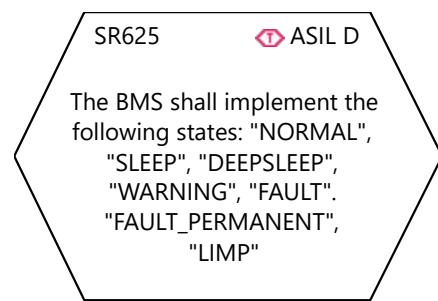
SR640  ASIL D
cyber security

SR641  ASIL D
Boot Loader

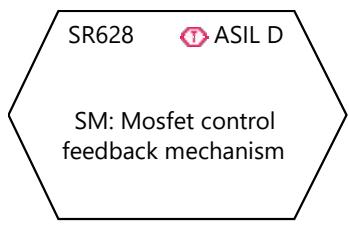
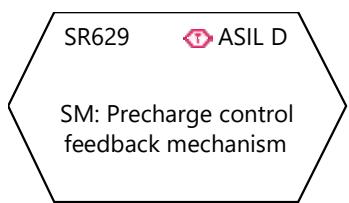
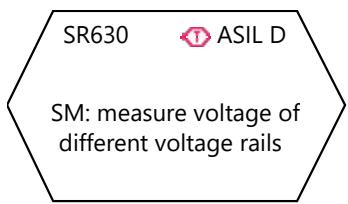
SR642  ASIL
Size

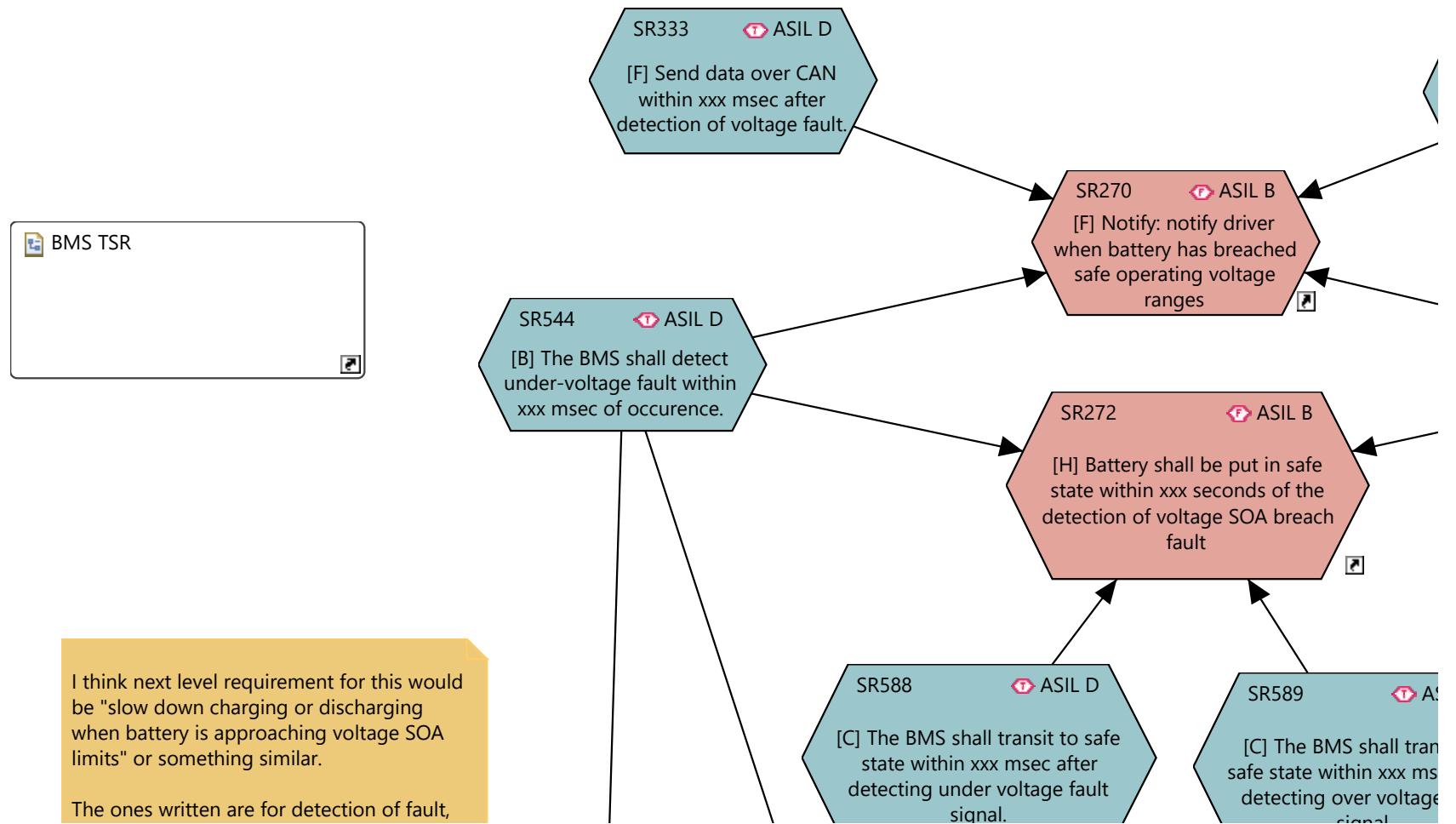
SR639  ASIL D
Access control

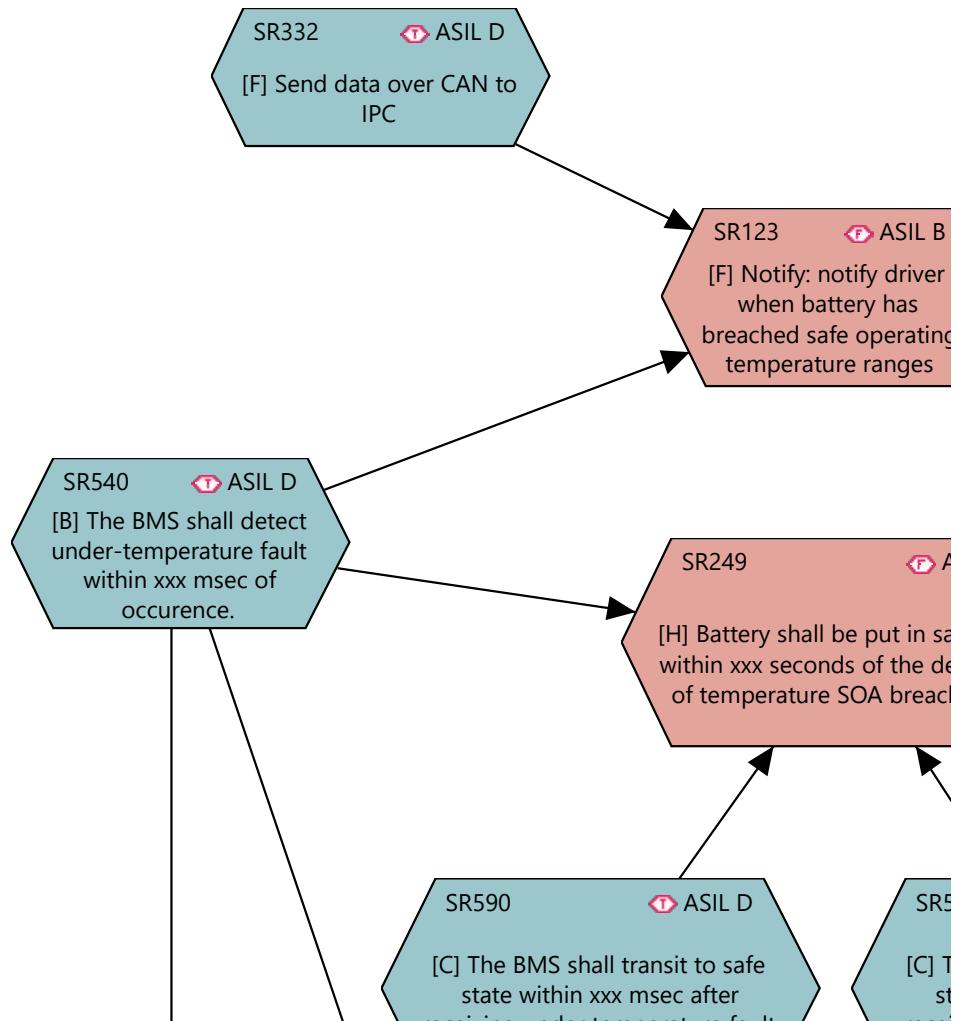
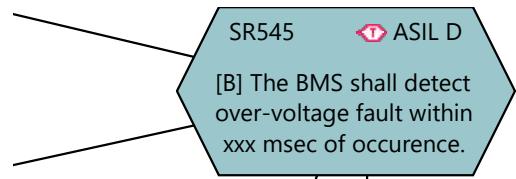
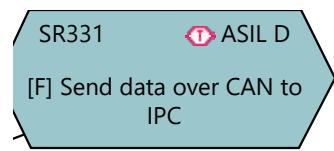


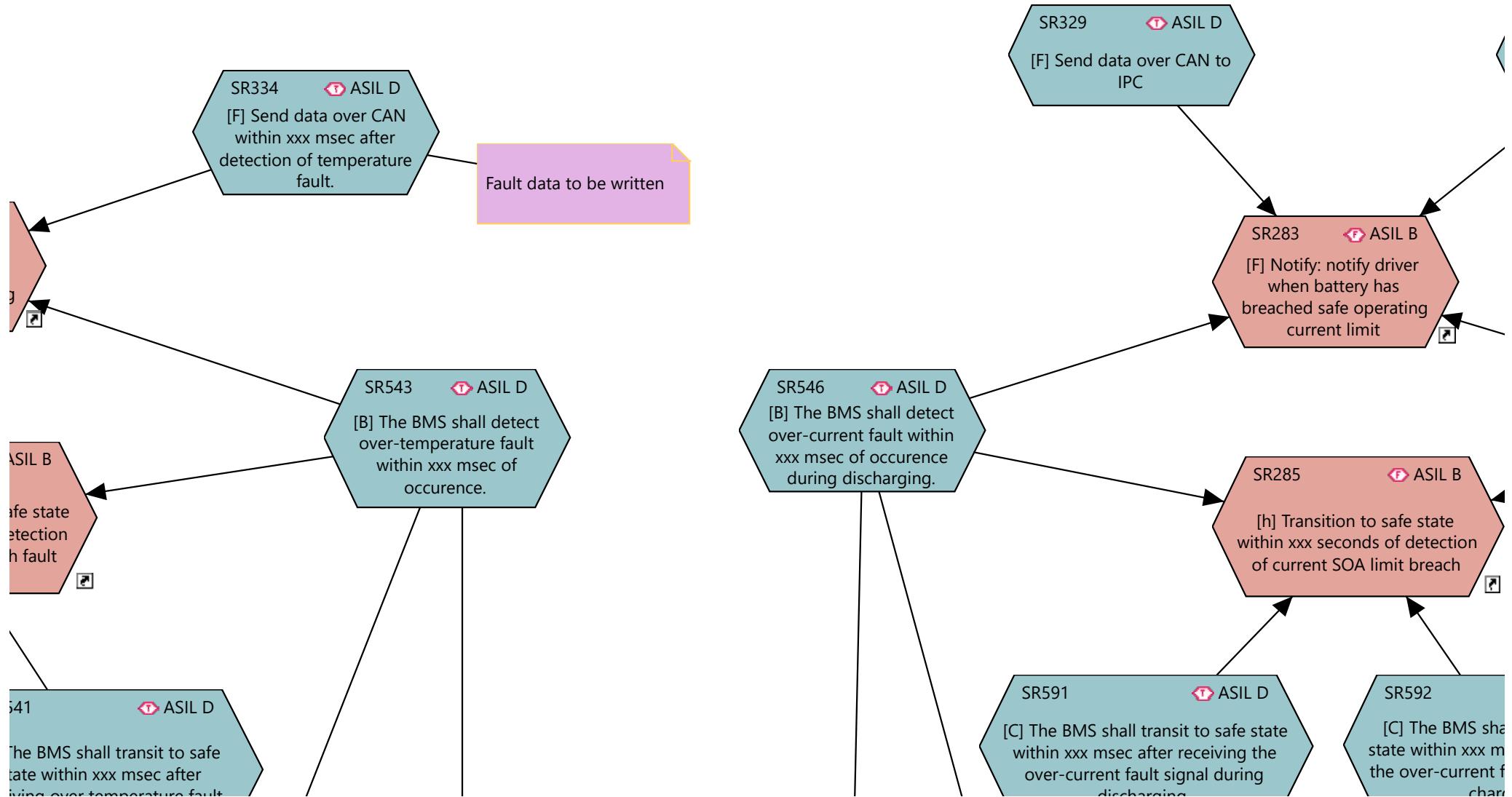


on









SR328  ASIL D
[F] Send data over CAN within xxx msec after detection of current fault.

Every fault have different threshold.

Correct wording

SR547  ASIL D
[B] The BMS shall detect over-current fault within xxx msec of occurrence during charging.

IN TSR define fault stages

 ASIL D
all transit to safe sec after receiving fault signal during

