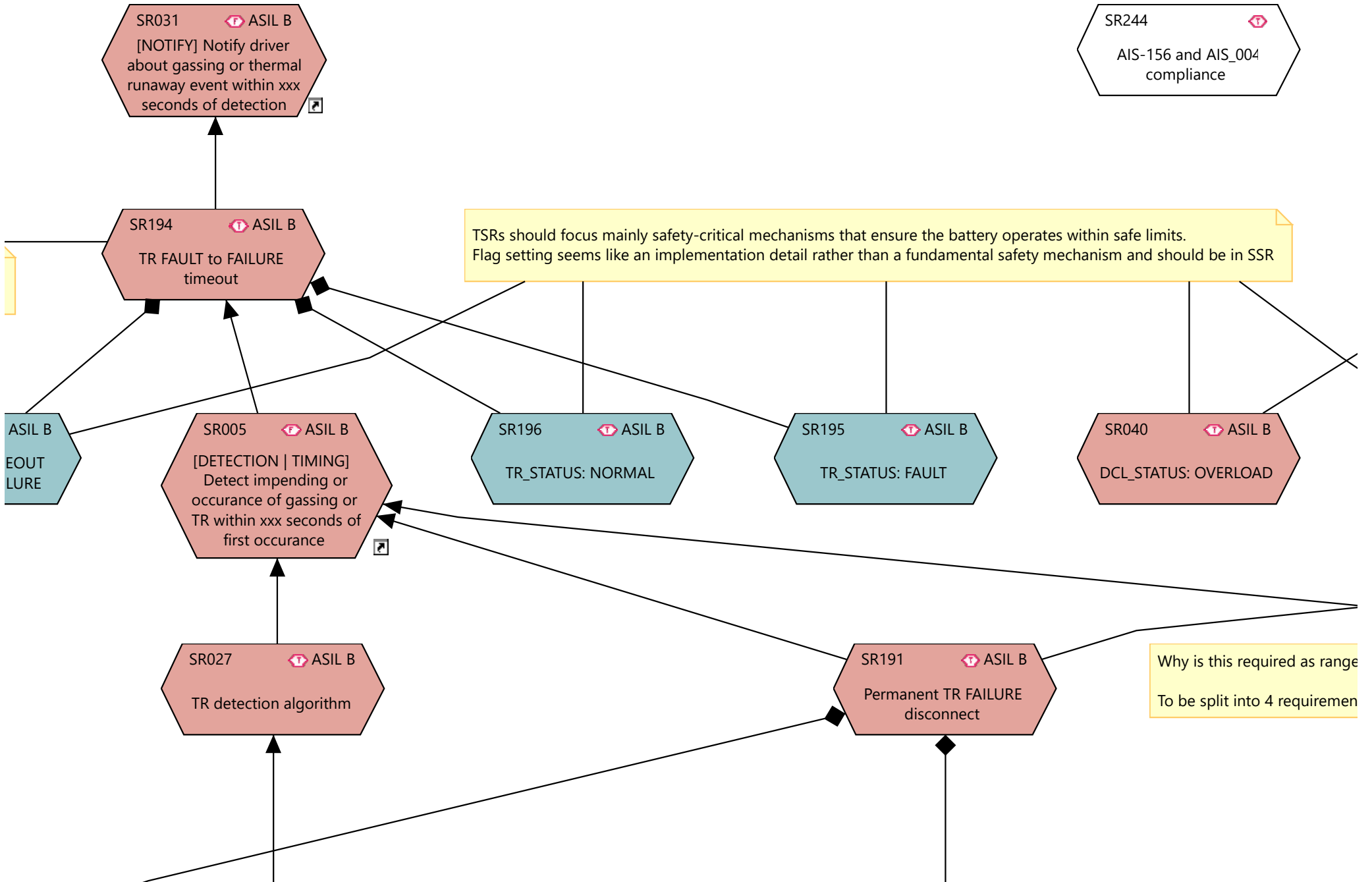


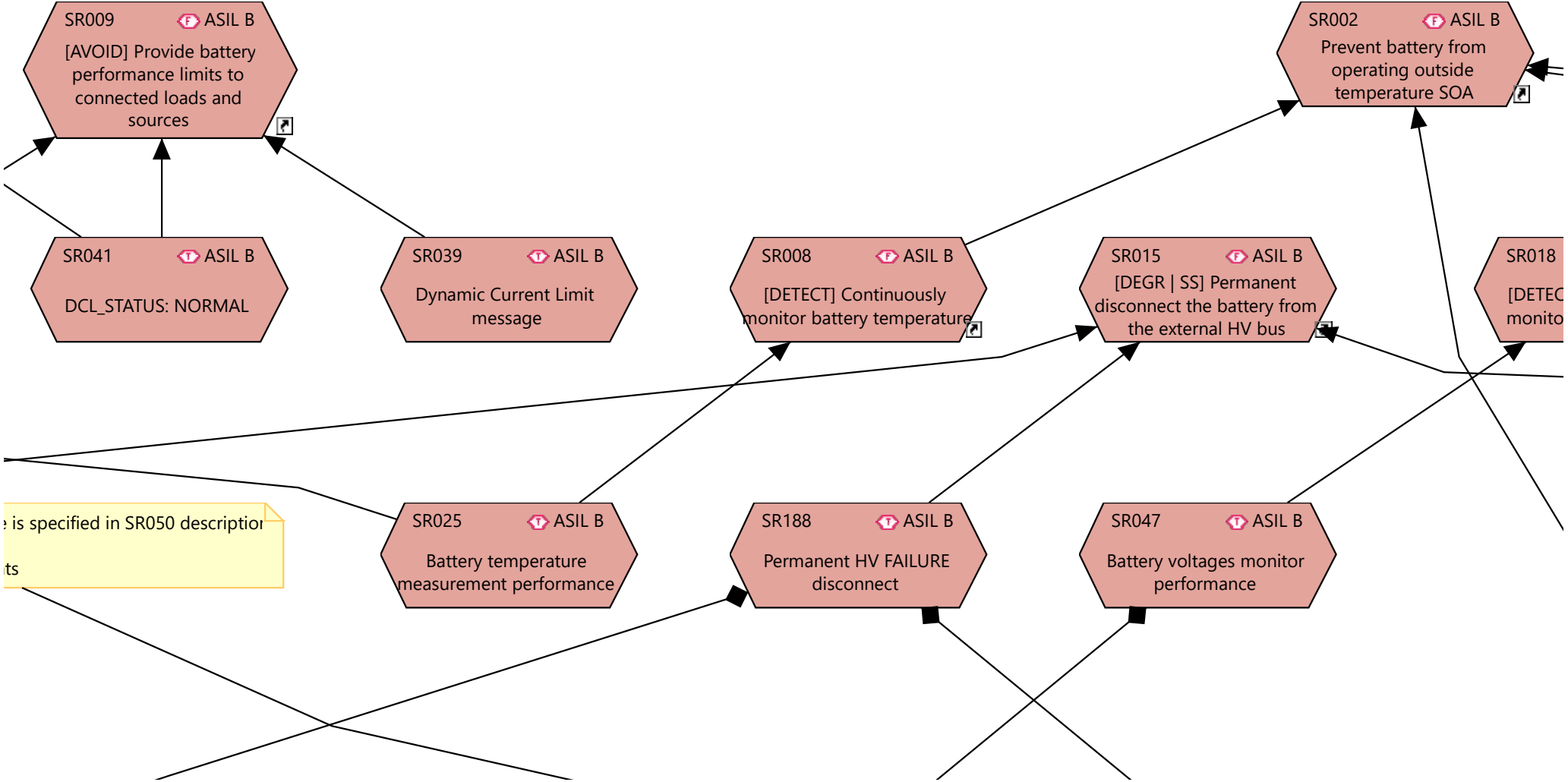
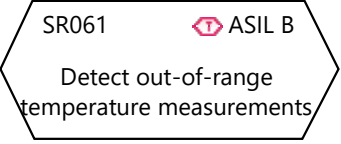
What is the need of both, they seem similar

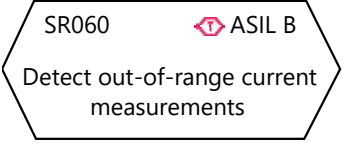
SR197



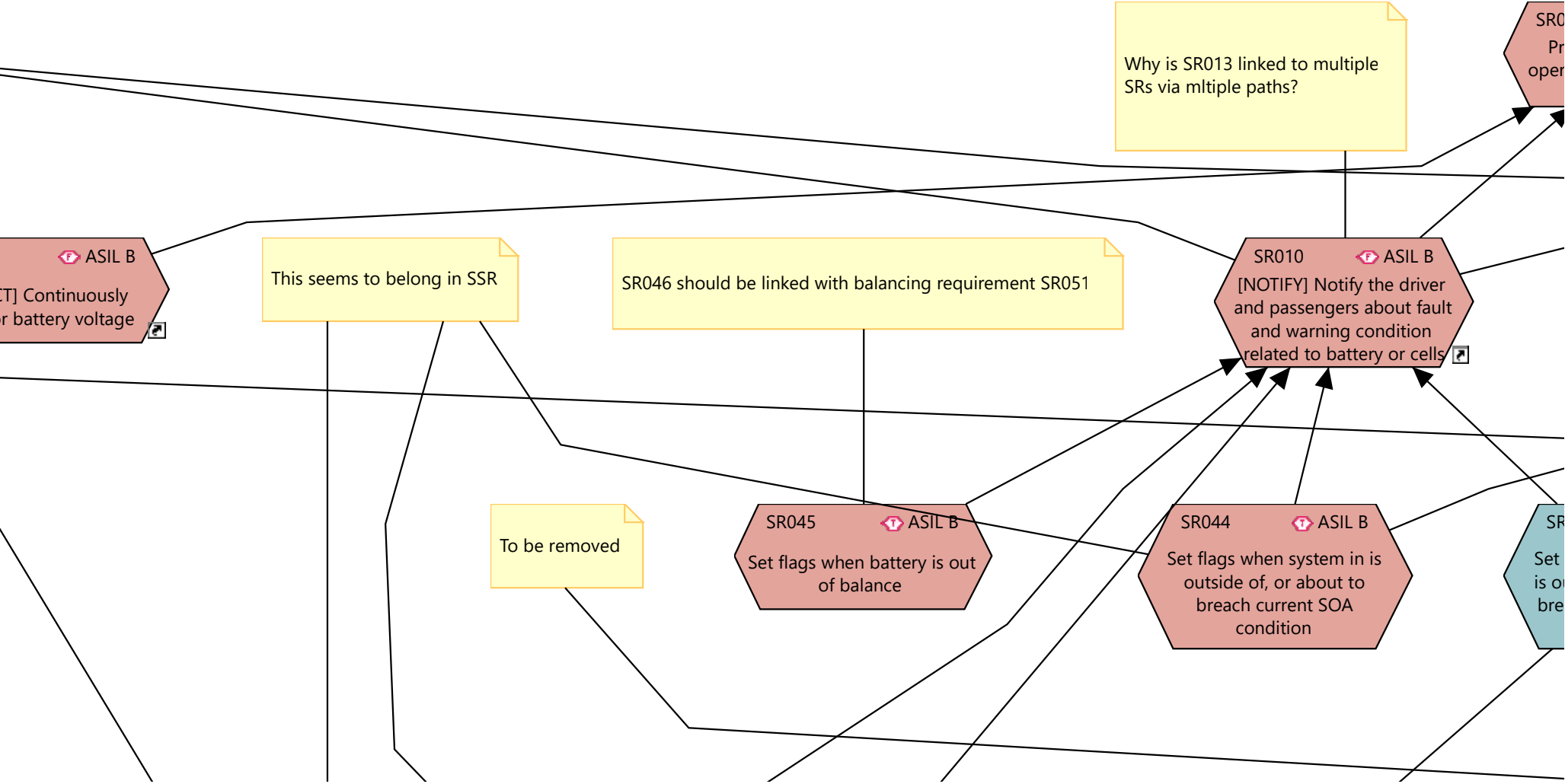
TR_FAILURE_TIM
, TR_STATUS: FAI







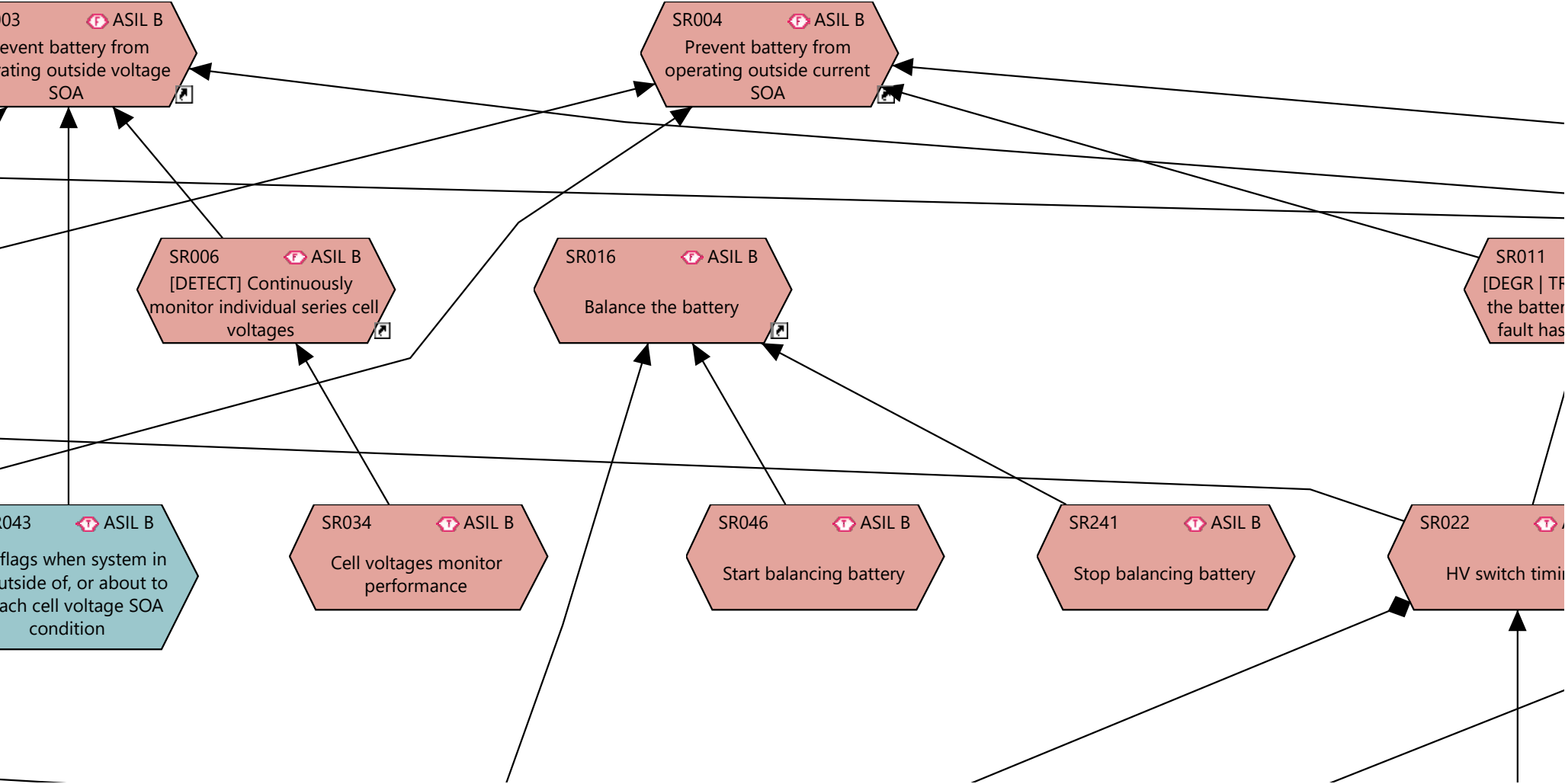
Is this a



invalid FSR as battery aging and wear affect performance and longevity but do not directly cause safety hazard:

SR063

Detect out-of-voltage meas



ASIL B
range cell
measurements

SR059 ASIL B
Detect out-of-range battery
voltage measurements

This should be a co

Should this be a TSR rather than FSR?

Separate

ASIL B
[R | SS] Disconnect
battery if over-current
has been detected

ASIL B
ng

SR238 ASIL B
Temporary disconnect the
battery if fault has been
detected related to battery
short circuit

SR202 ASIL B
Temporary disconnect the
battery if fault has been
detected related to battery
current

SR198 ASIL B
Temporary disconnect the
battery if fault has been
detected related to
temperature

SR199 ASIL B
Temporary disconnect the
battery if fault has been
detected related to cell
voltages

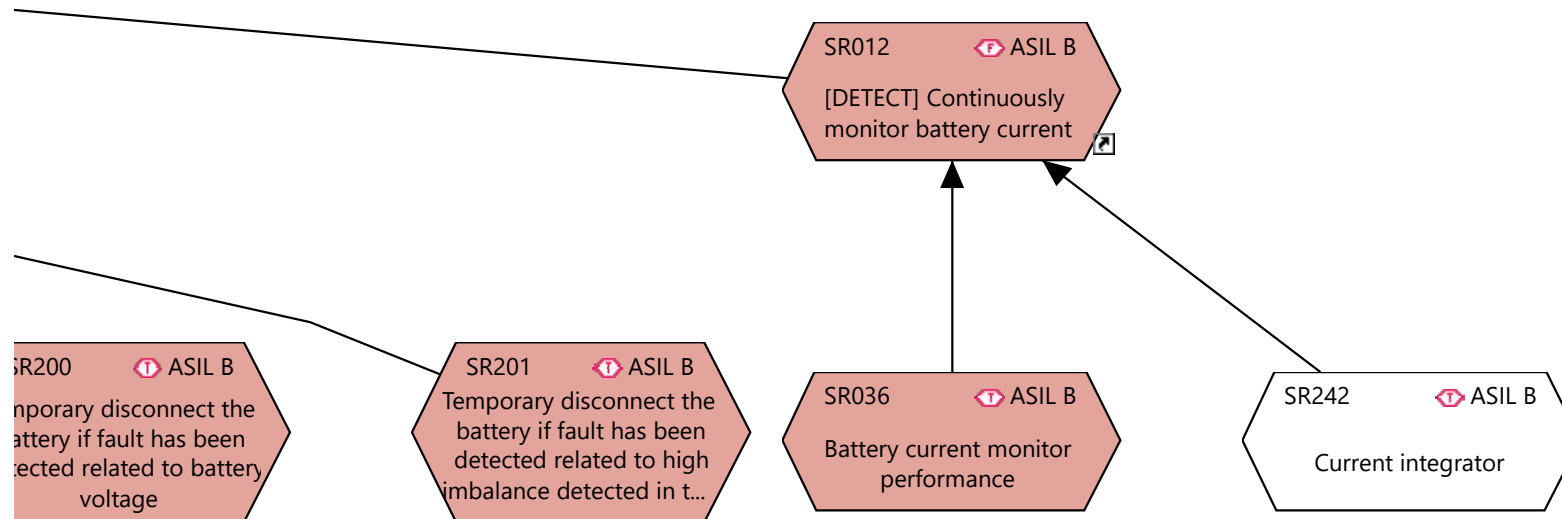
S
Ten
ba
det

SR007 ASIL B
[DEGR | TR | SS]
Temporarily disconnect
the battery if fault has
been detected

Common requirement for all faults and even other states

TSRs for individual faults of voltages and temperatures

Why are there separate TSR for each current fault ?
Why not compile those with voltage/temp related disconnects or
why not have separate TSR for each voltage/temp fault



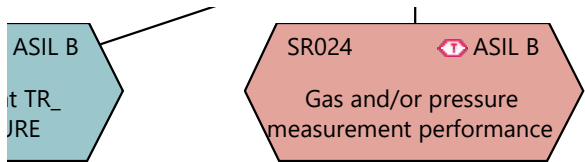
SR192



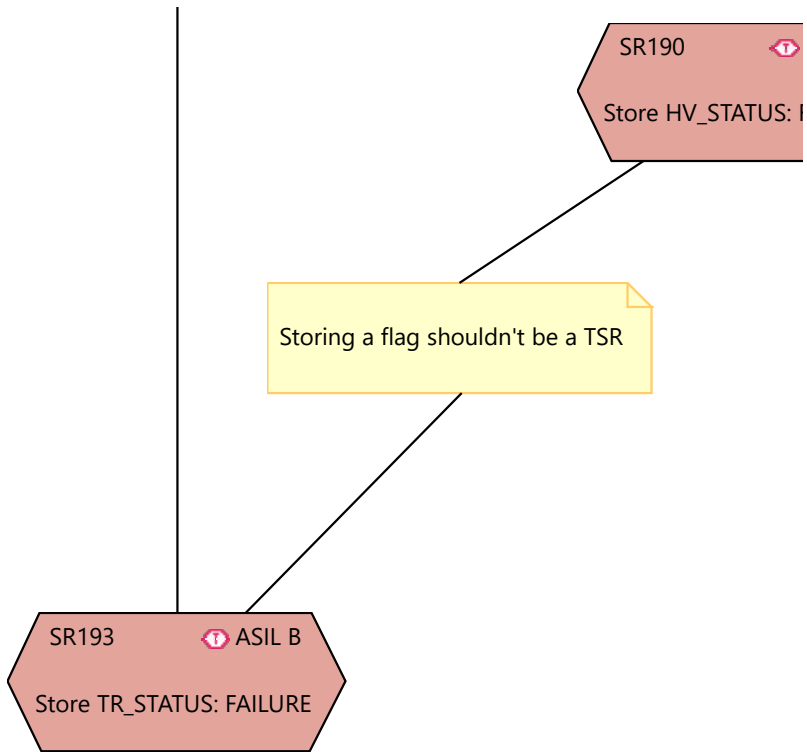
Open switches a
STATUS: FAILU

Why is opening switch
and not directly conne
temp cases

REMOVE THIS, this sho

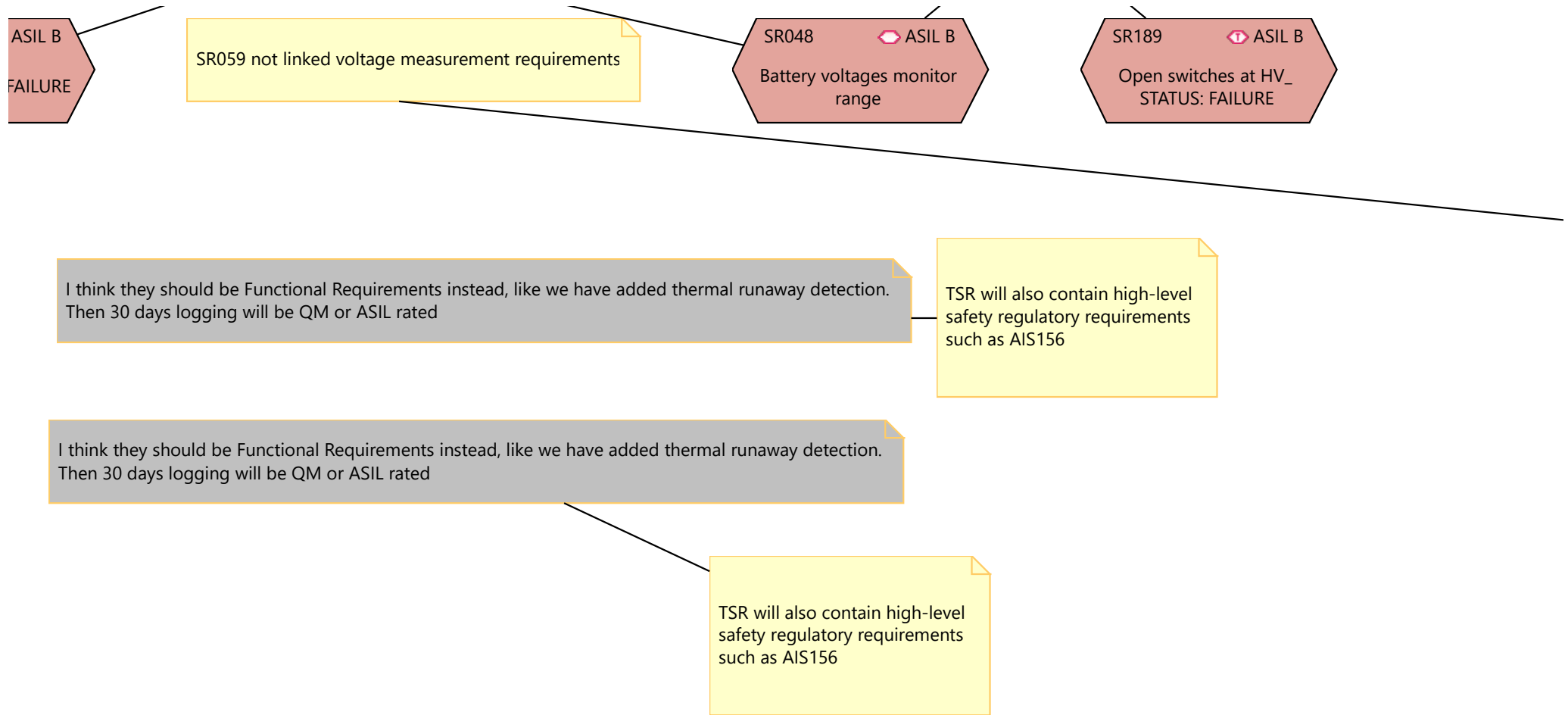


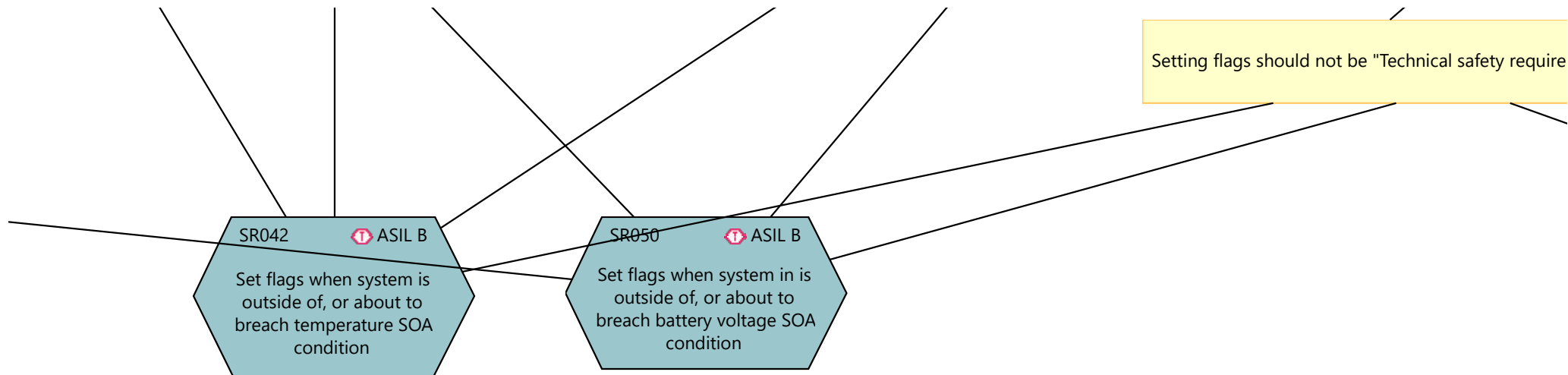
at TR a subrequirement of setting a flag
ected to TR detection. Like in voltage/
ould be SSR



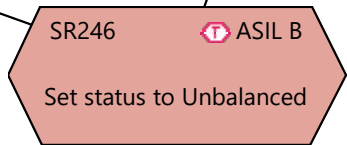
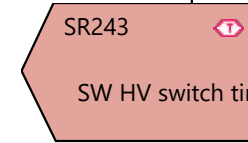
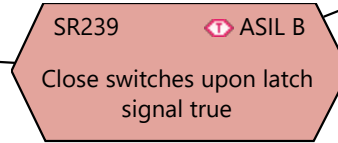
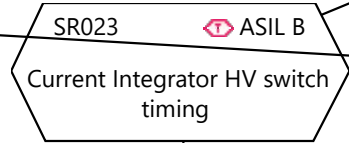
SR636 ASIL D

SR637 ASIL D





ment"

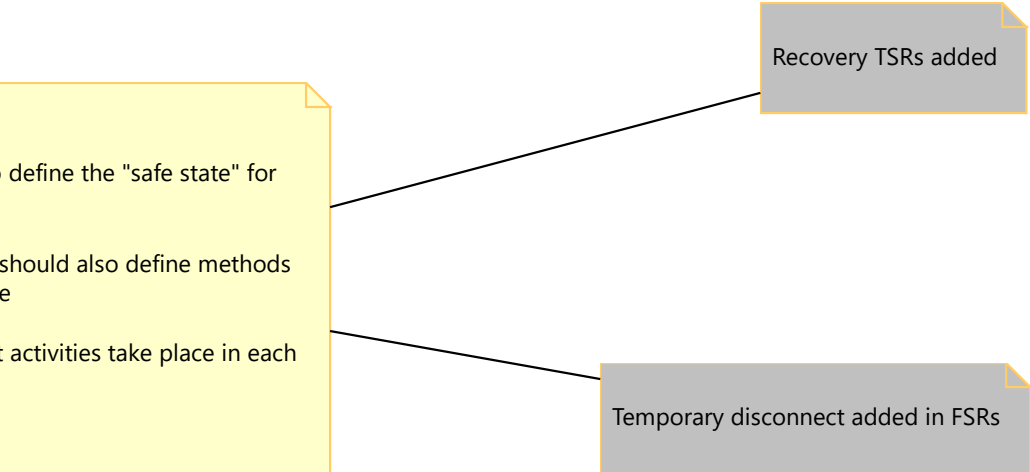
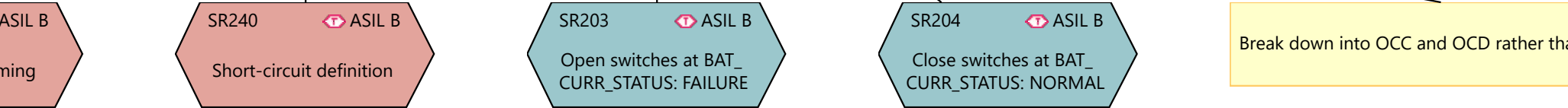


What does current integrator means here?
Should be a comparator instead?
To be removed

here we also need to
each fault.

Since this is TSR, we :
of exit from safe stati


And also define what
of those safe states








Reliability requirement:
signal connectors


Reliability requirement
shunt


SR635  ASIL D
Reliability requirement: main
mosfets


SR638  ASIL D
Reliability requirement
power connector


SR634  ASIL D
Reliability requirement:fuse


SR640  ASIL D
cyber security

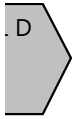
SR632  ASIL D
Reliability requirement:
precharge resistor

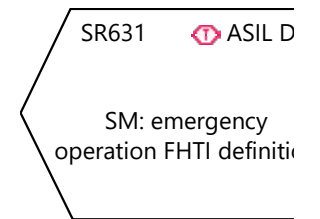
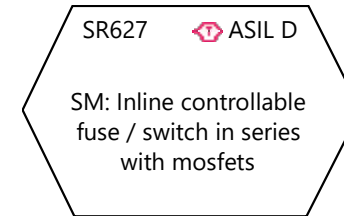
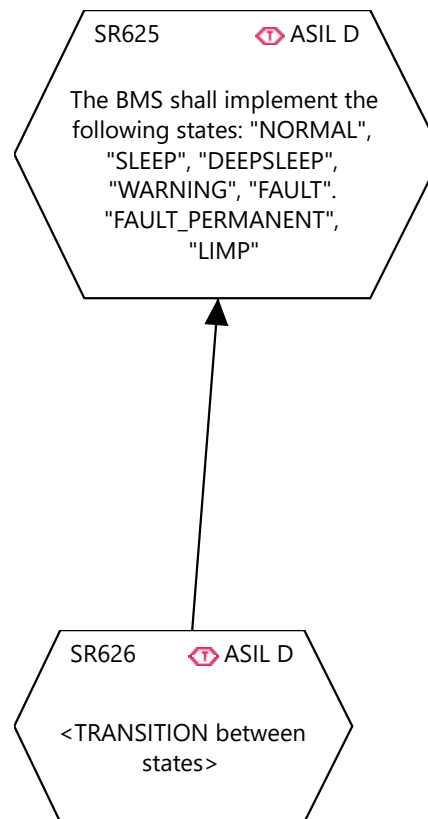
SR641  ASIL D
Boot Loader

SR633  ASIL D
Reliability requirement:
precharge mosfet

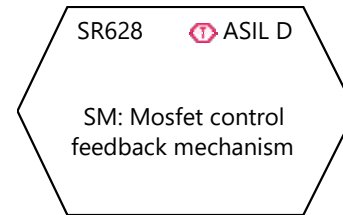
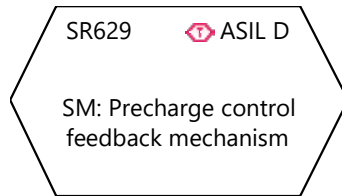
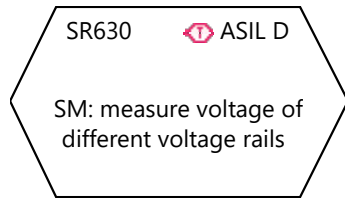
SR639  ASIL D
Access control

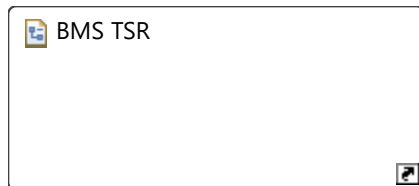
SR642  ASIL D
Size





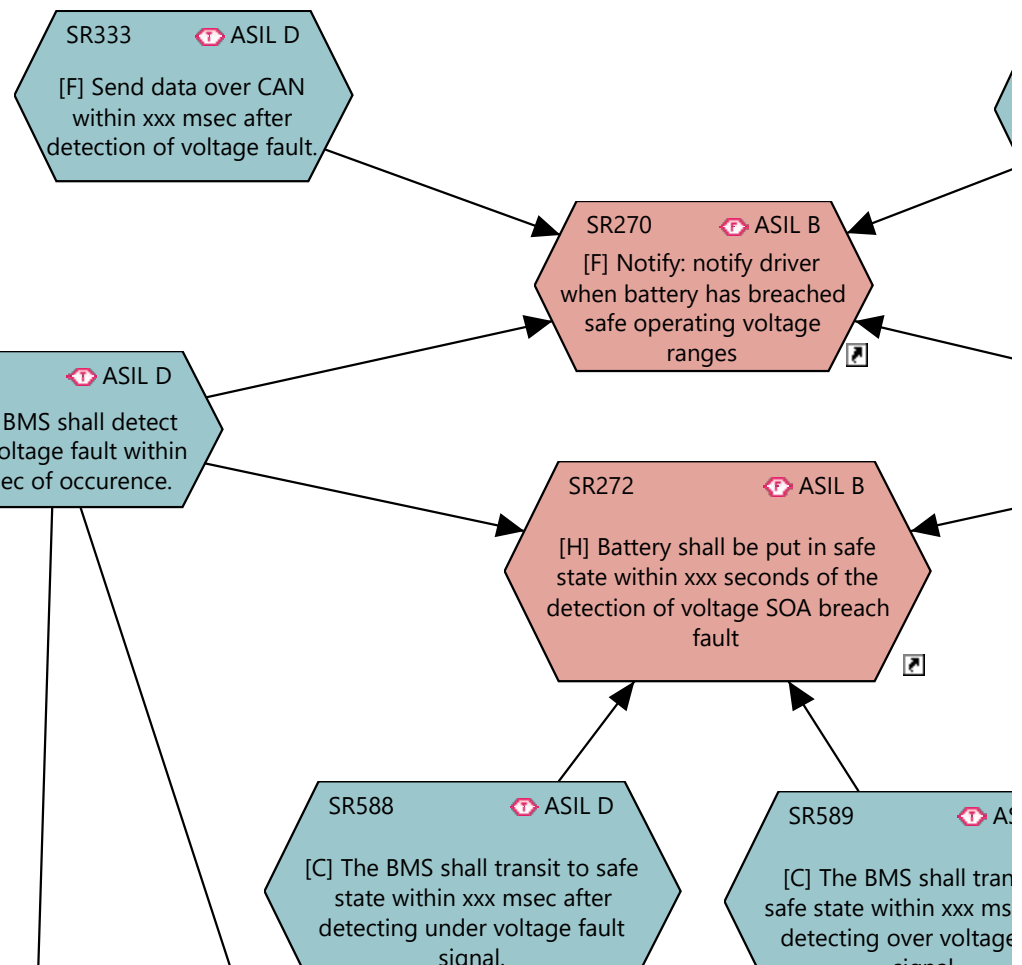
on

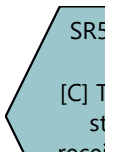
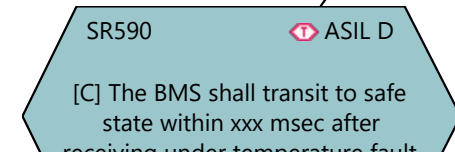
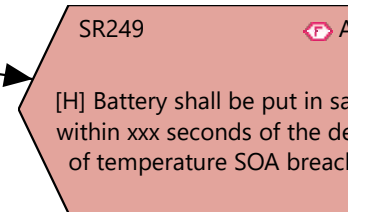
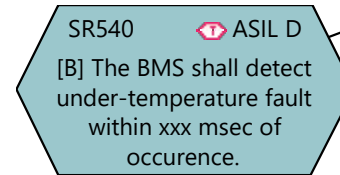
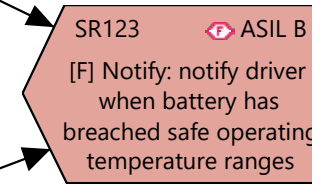
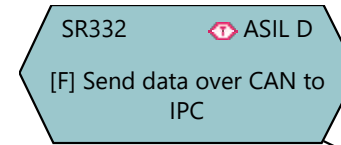
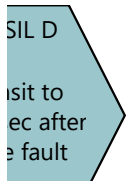
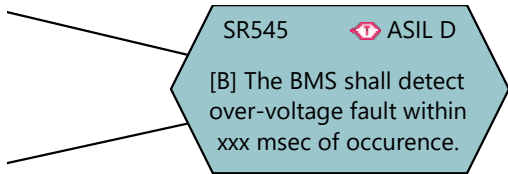
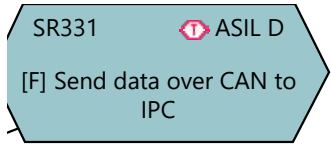


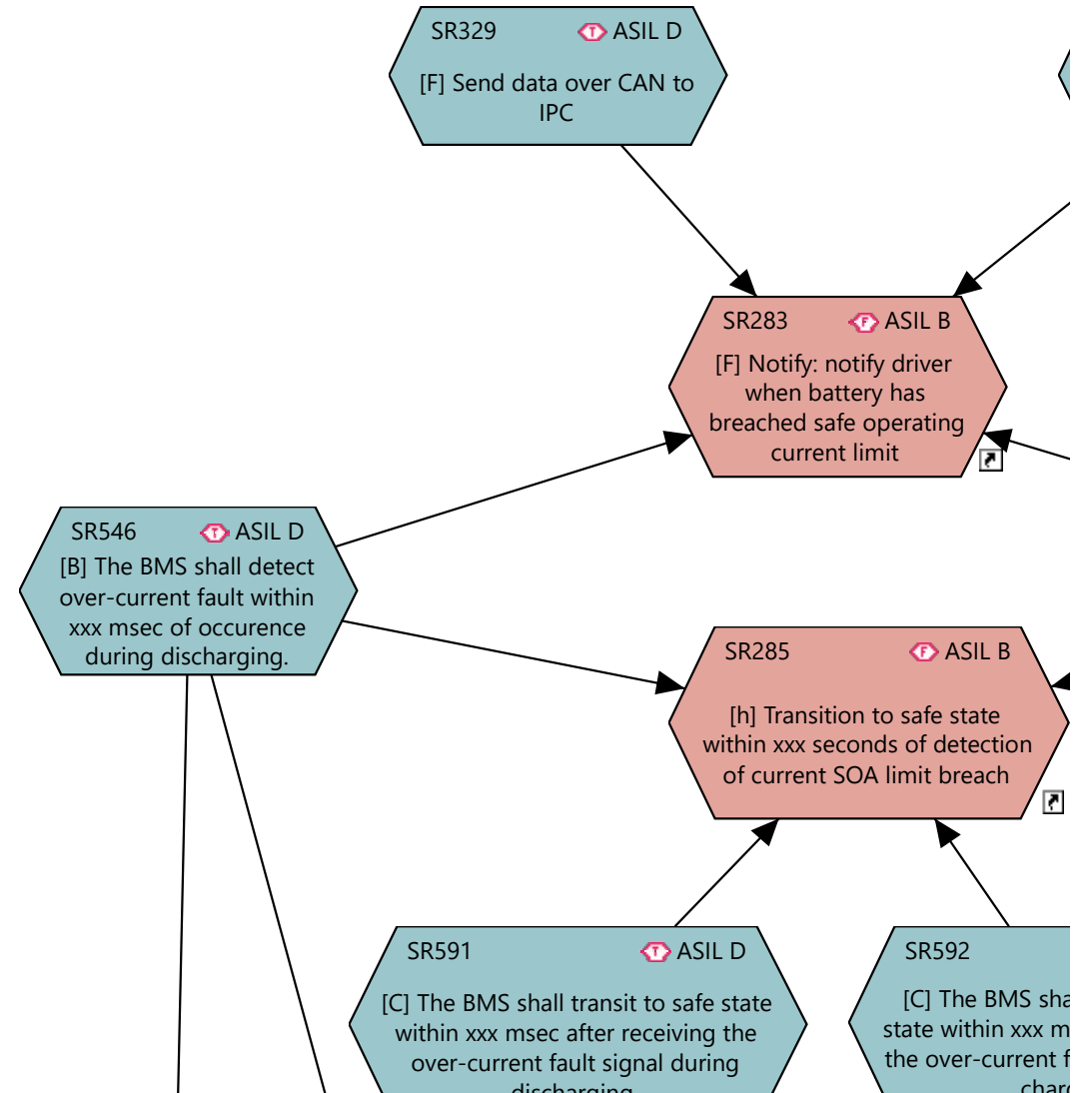
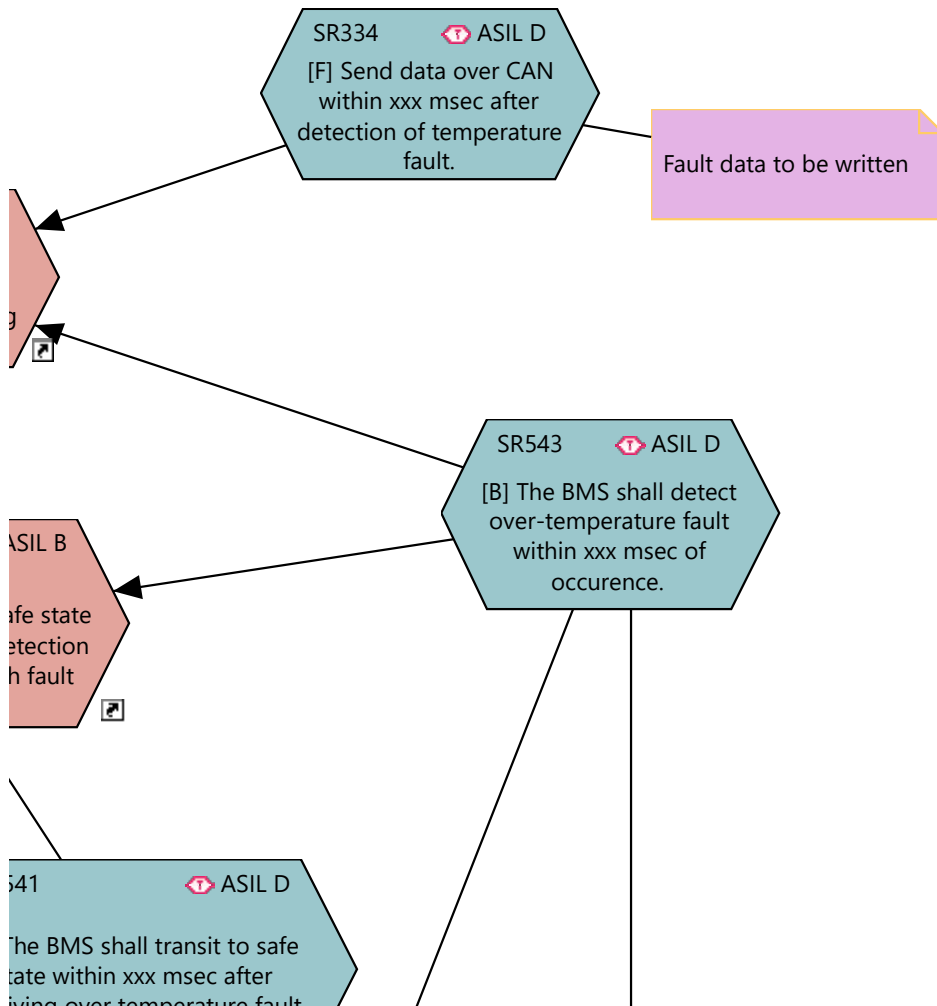



I think next level requirement for this would be "slow down charging or discharging when battery is approaching voltage SOA limits" or something similar.

The ones written are for detection of fault,









SR328  ASIL D
[F] Send data over CAN
within xxx msec after
detection of current fault.

Every fault have
different threshold.

Correct wording

SR547  ASIL D
[B] The BMS shall detect
over-current fault within
xxx msec of occurence
during charging.

IN TSR define fault stages

 ASIL D
all transit to safe
sec after receiving
fault signal during
charging

